



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Peter Hazucha et al.

Title: REGULATED SLEEP TRANSISTOR APPARATUS, METHOD, AND SYSTEM

Docket No.: 80107.040US1

Serial No.: 10/673822

Filed: September 29, 2003

Due Date: N/A

Examiner: Unknown

Group Art Unit: 2838

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Alexandria, VA 22313-1450

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By: \_\_\_\_\_

*Dana B. LeMoine*

Name: Dana LeMoine

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Name

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S/N 10/673822

PATENT

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COMMUNICATION RE: INCORRECT FILING RECEIPT

Commissioner for Patents  
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Alexandria, VA 22313-1450

Applicants hereby request correction of the Filing Receipt with respect to the above-identified patent application. In the Filing Receipt received January 9, 2004, (copy enclosed), the **name of the second inventor SHOULD READ Tanay Karnik** as evidenced by the Declaration and Power of Attorney document (copy enclosed).

Applicants would appreciate the above-identified error be corrected and that a new "corrected" filing receipt be sent to Applicants' representatives at the address given below.

Respectfully submitted,

PETER HAZUCHA ET AL.

By their Representatives,

LeMoine Patent Services, PLLC  
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952-473-8800

Date January 21, 2004

By

Dana B. LeMoine  
Dana LeMoine  
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Andrew J. Kaul  
Name

[Signature]  
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/673,822	09/29/2003	2838	1014	80107.040US1	8	30	4

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CONFIRMATION NO. 2204

## FILING RECEIPT



\*OC000000011614929\*

Date Mailed: 01/05/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

Peter Hazucha, Beaverton, OR;  
 Tanay Karnik, Portland, OR;

## Assignment For Published Patent Application

Intel Corporation;

## Domestic Priority data as claimed by applicant

## Foreign Applications

If Required, Foreign Filing License Granted: 01/03/2004

Projected Publication Date: 03/31/2005

Non-Publication Request: No

Early Publication Request: No

## Title

Regulated sleep transistor apparatus, method, and system

